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PETITION TO ACCEPT UNINTENTIONALLY DELAYED PAYMENT OF MAINTENANCE FEE IN AN EXPIRED PATENT (37 CFR 1.378 (c))Docket Number (Optional)
14485.12

Mail to: Mail Stop Petition
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450
 Fax: (571) 273-8300

NOTE: If information or assistance is needed in completing this form, please contact Petitions Information at (571) 272-3282.

Patent No. 6,891,424Application Number 09/408,114Issue Date 05-10-2005Filing Date 09-29-1999

CAUTION: Maintenance fee (and surcharge, if any) payment must correctly identify: (1) the patent number (or reissue patent number, if a reissue) and (2) the application number of the actual U.S. application (or reissue application) leading to issuance of that patent to ensure the fee(s) is/are associated with the correct patent. 37 CFR 1.368(c) and (d).

07/23/2009 DALLEN 00000069 502478 6891424

Also complete the following information, if applicable

91 FC:1599 2620.00 DA

The above - Identified patent

Is a reissue of original Patent No. _____ original issue date _____

original application number _____

original filing date _____

resulted from the entry into the U.S. under 35 U.S.C. 371 of international application _____

filed on _____

CERTIFICATE OF MAILING (37 CFR 1.89(a))

I hereby certify that this paper (*along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Mail Stop Petition, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

July 21, 2009

Date

Signature

Carlos R. Villamar, Reg. No. 43,224

Typed or Printed Name of Person Signing Certificate

[page 1 of 3]

This collection of information is required by 37 CFR 1.378(c). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Petition, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450

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1. SMALL ENTITY

Patentee claims, or has previously claimed, small entity status. See 37 CFR 1.27.

2. LOSS OF ENTITLEMENT TO SMALL ENTITY STATUS

Patentee is no longer entitled to small entity status. See 37 CFR 1.27(g)

3. MAINTENANCE FEE (37 CFR 1.20(e)-(g))

The appropriate maintenance fee must be submitted with this petition, unless it was paid earlier.

NOT Small Entity			Small Entity		
Amount	Fee	(Code)	Amount	Fee	(Code)
<input checked="" type="checkbox"/> \$ 980.00	3 1/2 yr fee	(1551)	<input type="checkbox"/> \$ _____	3 1/2 yr fee	(2551)
<input type="checkbox"/> \$ _____	7 1/2 yr fee	(1552)	<input type="checkbox"/> \$ _____	7 1/2 yr fee	(2552)
<input type="checkbox"/> \$ _____	11 1/2 yr fee	(1553)	<input type="checkbox"/> \$ _____	11 1/2 yr fee	(2553)

MAINTENANCE FEE BEING SUBMITTED \$ 980.00

4. SURCHARGE

The surcharge required by 37 CFR 1.20(i)(2) of \$ 1,640.00 (Fee Code 1558) must be paid as a condition of accepting unintentionally delayed payment of a maintenance fee.

SURCHARGE FEE BEING SUBMITTED \$ 1,640.00

5. MANNER OF PAYMENT

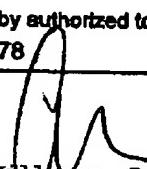
Enclosed is a check for the sum of \$ _____

Please charge Deposit Account No. 50-2478 the sum of \$ 2,620.00

Payment by credit card. Form PTO-2038 is attached.

6. AUTHORIZATION TO CHARGE ANY FEE DEFICIENCY

The Director is hereby authorized to charge any maintenance fee, surcharge or petition deficiency to Deposit Account No. 50-2478


Carlos R. Villemar, Reg. No. 43,224

JUL 21 2009

PTO/SB/88 (03-09)

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7. OVERPAYMENT

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OR

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Petitioner/applicant is cautioned to avoid submitting personal information in documents filed in a patent application that may contribute to identity theft. Personal information such as social security numbers, bank account numbers, or credit card numbers (other than a check or credit card authorization form PTO-2038 submitted for payment purposes) is never required by the USPTO to support a petition or an application. If this type of personal information is included in documents submitted to the USPTO, petitioners/applicants should consider redacting such personal information from the documents before submitting them to the USPTO. Petitioner/applicant is advised that the record of a patent application is available to the public after publication of the application (unless a non-publication request in compliance with 37 CFR 1.213(a) is made in the application or issuance of a patent. Furthermore, the record from an abandoned application may also be available to the public if the application is referenced in a published application or an issued patent (see 37 CFR 1.14). Checks and credit card authorization forms PTO-2038 submitted for payment purposes are not retained in the application file and therefore are not publicly available.

8. STATEMENT

The delay in payment of the maintenance fee to this patent was unintentional.

9. PETITIONER(S) REQUEST THAT THE DELAYED PAYMENT OF THE MAINTENANCE FEE BE ACCEPTED AND THE PATENT REINSTATEDTimothy Huang
Signature(s) of Petitioner(s)7-18-2009

Date

Mr. Timothy HUANG

Typed or printed name(s)

Registration Number, if applicable

Telephone Number

MICROELECTRONICS TECHNOLOGY, INC.

Address

Hsinchu, Taiwan, R.O.C.

Address

37 CFR 1.378(d) states: "Any petition under this section must be signed by an attorney or agent registered to practice before the Patent and Trademark Office, or by the patentee, the assignee, or other party in interest."

ENCLOSURES Maintenance Fee Payment Surcharge under 37 CFR 1.20(l)(2) (fee for filing the maintenance fee petition) Power of Attorney/Change of Correspondence Address and Statement
under 37 C.F.R. 3.73 (b)

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REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY
AND CHANGE OF CORRESPONDENCE ADDRESS

We hereby revoke all previous powers of attorney given in the US applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 and appoint the practitioners associated with the Customer Number 25570 to act on our behalf for each of the identified US applications and/or patents recorded by U.S. Patent and Trademark Office on July 13, 2009 in Reel/Frame 022939/0399.

Please change the correspondence address for each of the applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 for all purposes, including the fee address, to the address associated with Customer Number 25570.

Patent Owner, Microelectronic Technology, Inc., is the Assignee of record of the entire interest. A Statement under 37 C.F.R. 3.73(b) is enclosed. The undersigned is authorized to act on behalf of the assignee.

Signature

Mr. Timothy HUANG
Authorized representative of
MICROELECTRONICS TECHNOLOGY, INC.

Date

7-16-2009

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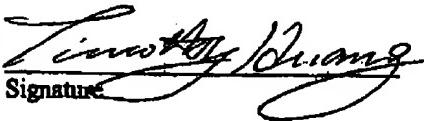
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

STATEMENT UNDER 37 CFR 3.73(b)

Patent Owner, Microelectronic Technology, Inc., a corporation, states that it is the assignee of the entire right, title, and interest in the U.S. patent applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399.

The undersigned is authorized to act on behalf of the assignee.



Signature

7-16-2009

Date

Mr. Timothy HUANG
Authorized representative of
MICROELECTRONICS TECHNOLOGY, INC.

Please address all the communication to the customer number 25570 associated with the below address:

Roberts Mlotkowski Safran & Cole P.C.
Intellectual Property Department
P.O. Box 10064
McLean, VA 22102
Telephone: 703-584-3270
Facsimile: 703-848-2981

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Patent Assignment Details

NOTE: Results display only for issued patents and published applications.
For pending or abandoned applications please consult USPTO staff.

Real/Frame: 022939/0399

Pages: 15

Attorney Dkt #: 14485.13

Recorded: 07/13/2009

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Total properties: 79

1	Patent #: 4941153	Issue Dt: 07/10/1990	Application #: 07089281	Filing Dt: 08/25/1987
	Title: HIGH-SPEED DIGITAL DATA COMMUNICATION SYSTEM			
2	Patent #: 4975931	Issue Dt: 12/04/1990	Application #: 07286435	Filing Dt: 12/19/1988
	Title: HIGH SPEED PROGRAMMABLE DIVIDER			
3	Patent #: 5058107	Issue Dt: 10/15/1991	Application #: 07293894	Filing Dt: 01/05/1989
	Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER			
4	Patent #: 5130578	Issue Dt: 07/14/1992	Application #: 07444454	Filing Dt: 11/30/1989
	Title: EFFICIENT HIGH SPEED N-WORD COMPARATOR			
5	Patent #: 5128534	Issue Dt: 07/07/1992	Application #: 07554238	Filing Dt: 07/17/1990
	Title: HIGH CHARGE CAPACITY FOCAL PLANE ARRAY READOUT CELL			
6	Patent #: 5271038	Issue Dt: 12/14/1993	Application #: 07580710	Filing Dt: 09/10/1990
	Title: DISTORTION SUPPRESSION USING THRESHOLDING TECHNIQUES			
7	Patent #: 5303161	Issue Dt: 04/12/1994	Application #: 07624958	Filing Dt: 12/10/1990
	Title: TECHNOLOGY INDEPENDENT INTEGRATED CIRCUIT MASK ARTWORK GENERATOR			
8	Patent #: 5164959	Issue Dt: 11/17/1992	Application #: 07643969	Filing Dt: 01/22/1991
	Title: DIGITAL EQUALIZATION METHOD AND APPARATUS			
9	Patent #: 5267272	Issue Dt: 11/30/1993	Application #: 07655684	Filing Dt: 02/14/1991
	Title: A RECEIVER AUTOMATIC GAIN CONTROL (AGC)			
10	Patent #: 5136205	Issue Dt: 08/04/1992	Application #: 07675590	Filing Dt: 03/26/1991
	Title: MICROELECTRONIC FIELD EMISSION DEVICE WITH AIR BRIDGE ANODE			
11	Patent #: 5128574	Issue Dt: 07/07/1992	Application #: 07676635	Filing Dt: 03/28/1991
	Title: TWO QUADRANTS HIGH SPEED MULTIPLYING DAC			
12	Patent #: 5205647	Issue Dt: 04/27/1993	Application #: 07722763	Filing Dt: 06/27/1991
	Title: LOW COST AGC FUNCTION FOR MULTIPLE APPROXIMATION A/D CONVERTERS			

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13	Patent #: <u>5251218</u>	Issue Dt: 10/05/1993	Application #: 07739593	Filing Dt: 07/31/1991
	Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER			
14	Patent #: <u>5220552</u>	Issue Dt: 06/15/1993	Application #: 07765157	Filing Dt: 09/23/1991
	Title: MULTIPLE USE DIGITAL TRANSMITTER/TRANSCIEVER WITH TIME MULTIPLEXING			
15	Patent #: <u>5304951</u>	Issue Dt: 04/19/1994	Application #: 07829183	Filing Dt: 01/31/1992
	Title: DIVIDER SYNCHRONIZATION CIRCUIT FOR PHASE-LOCKED LOOP FREQUENCY SYNTHESIZER			
16	Patent #: <u>5313113</u>	Issue Dt: 05/17/1994	Application #: 07870369	Filing Dt: 04/17/1992
	Title: SAMPLE AND HOLD CIRCUIT WITH FULL SIGNAL MODULATION COMPENSATION USING BIPOLOAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE			
17	Patent #: <u>5250911</u>	Issue Dt: 10/05/1993	Application #: 07871861	Filing Dt: 04/20/1992
	Title: SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT			
18	Patent #: <u>5315169</u>	Issue Dt: 05/24/1994	Application #: 07894980	Filing Dt: 06/08/1992
	Title: POWER-EFFICIENT SAMPLE AND HOLD CIRCUIT USING BIPOLOAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE			
19	Patent #: <u>5278837</u>	Issue Dt: 01/11/1994	Application #: 07905965	Filing Dt: 06/29/1992
	Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH COMBINED MULTIPLE FREQUENCY CHANNELS			
20	Patent #: <u>5350952</u>	Issue Dt: 09/27/1994	Application #: 07909286	Filing Dt: 07/06/1992
	Title: SAMPLE AND HOLD CIRCUIT WITH PUSH-PULL OUTPUT CHARGING CURRENT			
21	Patent #: <u>5315231</u>	Issue Dt: 05/24/1994	Application #: 07976760	Filing Dt: 11/16/1992
	Title: SYMMETRICAL BIPOLOAR BIAS CURRENT SOURCE WITH HIGH POWER SUPPLY REJECTION RATIO (PSRR)			
22	Patent #: <u>5621730</u>	Issue Dt: 04/15/1997	Application #: 07986180	Filing Dt: 12/07/1992
	Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING			
23	Patent #: <u>5483150</u>	Issue Dt: 01/09/1996	Application #: 08017200	Filing Dt: 02/05/1993
	Title: TRANSISTOR CURRENT SWITCH ARRAY FOR DIGITAL-TO-ANALOG CONVERTER (DAC) INCLUDING BIAS CURRENT COMPENSATION FOR INDIVIDUAL TRANSISTOR CURRENT GAIN AND THERMALLY INDUCED BASE-EMITTER VOLTAGE DROP VARIATION			
24	Patent #: <u>5343163</u>	Issue Dt: 08/30/1994	Application #: 08080269	Filing Dt: 06/21/1993
	Title: SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT			
25	Patent #: <u>5428305</u>	Issue Dt: 06/27/1995	Application #: 08129939	Filing Dt: 09/30/1993
	Title: DIFFERENTIAL LOGIC LEVEL TRANSLATOR CIRCUIT WITH DUAL OUTPUT LOGIC LEVELS SELECTABLE BY POWER CONNECTOR OPTIONS			
26	Patent #: <u>5410274</u>	Issue Dt: 04/25/1995	Application #: 08210269	Filing Dt: 03/17/1994
	Title: SINGLE-ENDED AND DIFFERENTIAL AMPLIFIERS WITH HIGH FEEDBACK INPUT IMPEDANCE AND LOW DISTORTION			
27	Patent #: <u>5592181</u>	Issue Dt: 01/07/1997	Application #: 08443519	Filing Dt: 05/18/1995
	Title: VEHICLE POSITION TRACKING TECHNIQUE			
28	Patent #: <u>5572220</u>	Issue Dt: 11/05/1996	Application #: 08443537	Filing Dt: 05/18/1995
	Title: TECHNIQUE TO DETECT ANGLE OF ARRIVAL WITH LOW AMBIGUITY			
29	Patent #: <u>5581213</u>	Issue Dt: 12/03/1996	Application #: 08479284	Filing Dt: 06/07/1995
	Title: VARIABLE GAIN AMPLIFIER CIRCUIT			
30	Patent #: <u>5848160</u>	Issue Dt: 12/08/1998	Application #: 08603673	Filing Dt: 02/20/1996
	Title: DIGITAL SYNTHESIZED WIDEBAND NOISE-LIKE WAVEFORM			

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31	Patent #: <u>5729576</u>	Issue Dt: 03/17/1998	Application #: 08641452	Filing Dt: 04/30/1996
	Title: INTERFERENCE CANCELING RECEIVER			
32	Patent #: <u>5684435</u>	Issue Dt: 11/04/1997	Application #: 08653930	Filing Dt: 05/22/1996
	Title: ANALOG WAVEFORM COMMUNICATIONS REDUCED INSTRUCTION SET PROCESSOR			
33	Patent #: <u>5856760</u>	Issue Dt: 01/05/1999	Application #: 08745070	Filing Dt: 11/07/1996
	Title: OVERDRIVE PROTECTION CLAMP SCHEME FOR FEEDBACK AMPLIFIERS			
34	Patent #: <u>5774318</u>	Issue Dt: 06/30/1998	Application #: 08753647	Filing Dt: 11/27/1996
	Title: I.C. POWER SUPPLY TERMINAL PROTECTION CLAMP			
35	Patent #: <u>5860040</u>	Issue Dt: 09/28/1999	Application #: 08761103	Filing Dt: 12/05/1996
	Title: COMMUNICATION SIGNAL PROCESSORS AND METHODS			
36	Patent #: <u>5870492</u>	Issue Dt: 02/09/1999	Application #: 08764808	Filing Dt: 12/12/1996
	Title: MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING			
37	Patent #: <u>5859568</u>	Issue Dt: 01/12/1999	Application #: 08827854	Filing Dt: 04/11/1997
	Title: TEMPERATURE COMPENSATED AMPLIFIER			
38	Patent #: <u>5859558</u>	Issue Dt: 01/12/1999	Application #: 08827855	Filing Dt: 04/11/1997
	Title: LOW VOLTAGE ANALOG FRONT END			
39	Patent #: <u>5859569</u>	Issue Dt: 01/12/1999	Application #: 08843200	Filing Dt: 04/14/1997
	Title: CURRENT FEEDBACK DIFFERENTIAL AMPLIFIER CLAMP			
40	Patent #: <u>6040731</u>	Issue Dt: 03/21/2000	Application #: 08848930	Filing Dt: 05/01/1997
	Title: DIFFERENTIAL PAIR GAIN CONTROL STAGE			
41	Patent #: <u>5859559</u>	Issue Dt: 01/12/1999	Application #: 08903657	Filing Dt: 07/31/1997
	Title: MIXER STRUCTURES WITH ENHANCED CONVERSION GAIN REDUCED SPURIOUS SIGNALS			
42	Patent #: <u>6118811</u>	Issue Dt: 09/12/2000	Application #: 08903807	Filing Dt: 07/31/1997
	Title: SELF-CALIBRATING, SELF-CORRECTING TRANSCEIVERS AND METHODS			
43	Patent #: <u>5990815</u>	Issue Dt: 11/23/1999	Application #: 08941457	Filing Dt: 09/30/1997
	Title: MONOLITHIC CIRCUIT AND METHOD FOR ADDING A RANDOMIZED DITHER SIGNAL TO THE FINE QUANTIZER ELEMENT OF A SUBRANGING ANALOG-TO-DIGITAL CONVERTER (ADC)			
44	Patent #: <u>5995535</u>	Issue Dt: 11/30/1999	Application #: 08965251	Filing Dt: 11/06/1997
	Title: RAPID TIME AND FREQUENCY ACQUISITION OF SPREAD SPECTRUM WAVEFORMS VIA AMBIGUITY TRANSFORM			
45	Patent #: <u>5926123</u>	Issue Dt: 07/20/1999	Application #: 08986942	Filing Dt: 12/08/1997
	Title: SELF CALIBRATION CIRCUITRY AND ALGORITHM FOR MULTIPASS ANALOG TO DIGITAL CONVERTER INTERSTAGE GAIN CORRECTION			
46	Patent #: <u>5973631</u>	Issue Dt: 10/26/1999	Application #: 09009612	Filing Dt: 01/20/1998
	Title: TEST CIRCUIT AND METHOD OF TRIMMING A UNARY DIGITAL-TO- ANALOG CONVERTER (DZC) IN A SUBRANGING ANALOG-TO-DIGITAL CONVERTER (ADC)			
47	Patent #: <u>5963094</u>	Issue Dt: 10/05/1999	Application #: 09027241	Filing Dt: 02/20/1998
	Title: MONOLITHIC CLASS AB SHUNT-SHUNT FEEDBACK CMOS LOW NOISE AMPLIFIER HAVING SELF BIAS			
48	Patent #: <u>6157234</u>	Issue Dt: 12/05/2000	Application #: 09219759	Filing Dt: 12/23/1998
	Title: HIGH SPEED PIN DRIVER INTEGRATED CIRCUIT ARCHITECTURE FOR COMMERCIAL AUTOMATIC TEST EQUIPMENT APPLICATIONS			

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	Patent #:	Issue Dt:	Application #:	Filing Dt:
49	<u>6636730</u> Publication #: US20020055347	10/21/2003 Pub Dt: 05/09/2002	09220288	12/23/1998
	Title: WIDEBAND IF IMAGE REJECTING RECEIVER			
50	<u>6891424</u>	05/10/2005	09408114	09/29/1999
	Title: MONOLITHIC PAYLOAD IF SWITCH			
51	<u>6552343</u>	04/22/2003	09574123	05/18/2000
	Title: UNIT CELL WITH FAN-OUT FOR LARGE FOCAL PLANE ARRAYS WITH SMALL DETECTOR PITCH			
52	<u>6931083</u>	08/16/2005	09579596	05/26/2000
	Title: LOW NOISE, LOW DISTORTION, MUXABLE GILBERT MIXER SIGNAL PROCESSING SYSTEM AND METHOD WITH AGC FUNCTIONALITY			
53	<u>6535062</u>	03/18/2003	09607223	06/30/2000
	Title: Low noise, low distortion, complementary IF amplifier			
54	<u>6693980</u>	02/17/2004	09664298	09/18/2000
	Title: WIDEBAND FAST-HOPPING RECEIVER FRONT-END AND MIXING METHOD			
55	<u>6580383</u>	06/17/2003	09703646	11/01/2000
	Title: HIGH RESOLUTION ADC BASED ON AN OVERSAMPLED SUBRANGING ADC			
56	<u>6975189</u>	12/13/2005	09705134	11/02/2000
	Title: ON-CHIP MULTILAYER METAL SHIELDED TRANSMISSION LINE			
57	<u>6400229</u>	05/04/2002	09790796	02/22/2001
	Title: LOW NOISE, LOW DISTORTION RF AMPLIFIER TOPOLOGY			
58	<u>6717450</u>	04/06/2004	10144175	05/13/2002
	Title: MONOLITHIC I-LOAD ARCHITECTURE FOR AUTOMATIC TEST EQUIPMENT			
59	<u>6683904</u> Publication #: US20030210737	01/27/2004 Pub Dt: 11/13/2003	10144329	05/13/2002
	Title: RF TRANSCEIVER WITH LOW POWER CHIRP ACQUISITION MODE			
60	<u>6882294</u> Publication #: US20050030216	04/19/2005 Pub Dt: 02/10/2005	10635826	08/06/2003
	Title: RESISTIVE LADDER, SUMMING NODE CIRCUIT, AND TRIMMING METHOD FOR A SUBRANGING ANALOG TO DIGITAL CONVERTER			
61	<u>5825697</u>	11/30/2004	10689496	10/20/2003
	Title: HIGH-PERFORMANCE TRACK AND HOLD CIRCUIT			
62	<u>NONE</u> Publication #: US20040257125	Issue Dt: Pub Dt: 12/23/2004	10698257	10/30/2003
	Title: DIGITAL-TO-ANALOG CONVERTER WITH ALWAYS-ON CASCODE TRANSISTORS			
63	<u>7095347</u> Publication #: US20040257058	08/22/2006 Pub Dt: 12/23/2004	10715262	11/17/2003
	Title: DIGITALLY TRIMMED DAC CELL			
64	<u>6879276</u> Publication #: US20050035892	04/12/2005 Pub Dt: 02/17/2005	10739860	12/18/2003
	Title: SPLIT CELL BOWTIE DIGITAL TO ANALOG CONVERTER AND METHOD			

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	Patent #: <u>7071781</u> Publication #: <u>US20050035821</u>	Issue Dt: 07/04/2006 Pub Dt: 02/17/2005	Application #: 10740172	Filing Dt: 12/18/2003
65	Title: HIGH SPEED, HIGH RESOLUTION AMPLIFIER TOPOLOGY			
	Patent #: <u>7098684</u> Publication #: <u>US20050035790</u>	Issue Dt: 08/29/2006 Pub Dt: 02/17/2005	Application #: 10740173	Filing Dt: 12/18/2003
66	Title: HIGH SPEED SWITCH			
	Patent #: NONE Publication #: <u>US20050035788</u>	Issue Dt: Pub Dt: 02/17/2005	Application #: 10740334	Filing Dt: 12/18/2003
67	Title: Clamped comparator			
	Patent #: NONE Publication #: <u>US20050038846</u>	Issue Dt: Pub Dt: 02/17/2005	Application #: 10847433	Filing Dt: 05/17/2004
68	Title: Subtraction circuit with a dummy digital to analog converter			
	Patent #: <u>7088148</u> Publication #: <u>US20050035791</u>	Issue Dt: 08/08/2006 Pub Dt: 02/17/2005	Application #: 10863561	Filing Dt: 06/08/2004
69	Title: SAMPLE AND HOLD CIRCUIT AND BOOTSTRAPPING CIRCUITS THEREFOR			
	Patent #: <u>7098700</u> Publication #: <u>US20050127955</u>	Issue Dt: 08/29/2006 Pub Dt: 06/16/2005	Application #: 10866850	Filing Dt: 07/08/2004
70	Title: LOW POWER OUTPUT DRIVER			
	Patent #: <u>7154421</u> Publication #: <u>US20050128118</u>	Issue Dt: 12/26/2006 Pub Dt: 06/16/2005	Application #: 10890443	Filing Dt: 07/12/2004
71	Title: DNL/INL TRIM TECHNIQUES FOR COMPARATOR BASED ANALOG TO DIGITAL CONVERTERS			
	Patent #: NONE Publication #: <u>US20050083223</u>	Issue Dt: Pub Dt: 04/21/2005	Application #: 10967963	Filing Dt: 10/19/2004
72	Title: Resolution enhanced folding amplifier			
	Patent #: <u>7253689</u> Publication #: <u>US20050270107</u>	Issue Dt: 08/07/2007 Pub Dt: 12/08/2005	Application #: 11148683	Filing Dt: 06/08/2005
73	Title: LOW DISTORTION AMPLIFIER			
	Patent #: NONE Publication #: <u>US20050078065</u>	Issue Dt: Pub Dt: 04/13/2005	Application #: 11150445	Filing Dt: 06/09/2005
74	Title: DIGITAL PRE-DISTORTION TECHNIQUE USING NONLINEAR FILTERS			
	Patent #: NONE Publication #: <u>US20050120479</u>	Issue Dt: Pub Dt: 06/08/2005	Application #: 11246027	Filing Dt: 10/07/2005
75	Title: System and method for crest factor reduction			
	Patent #: NONE Publication #: <u>US20050247487</u>	Issue Dt: Pub Dt: 10/09/2005	Application #: 11784433	Filing Dt: 04/05/2007
76	Title: Dynamic crest factor reduction system			
	Patent #: NONE Publication #: <u>US20050260066</u>	Issue Dt: Pub Dt: 10/23/2005	Application #: 11788451	Filing Dt: 04/20/2007
77	Title: Dynamic digital pre-distortion system			

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Patent #: NONE **Issue Dt:** **Application #:** 11951238 **Filing Dt:** 12/05/2007
78 Publication #: US20080095265 **Pub Dt:** 04/24/2008
Title: DIGITAL PRE-DISTORTION TECHNIQUE USING NON-LINEAR FILTERS

Patent #: NONE **Issue Dt:** **Application #:** 12031249 **Filing Dt:** 02/14/2008
79 Publication #: US20080211583 **Pub Dt:** 09/04/2008
Title: SYSTEM AND METHOD FOR DYNAMIC DRAIN VOLTAGE ADJUSTMENT TO CONTROL LINEARITY, OUTPUT POWER, AND EFFICIENCY IN RF POWER AMPLIFIERS

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